## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

**MEARS** 

Sérial No. 10/647,061

JUN 2 5 2004 Confirmation No. 9705

Filing Date: AUGUST 22, 2003

) Examiner: S. Mulpuri

) Art Unit: 2812

for: **METHOD FOR MAKING SEMICONDUCTOR**) Attorney Docket:

DEVICE INCLUDING BAND-ENGINEERED SUPERLATTICE 62603

## CITATION UNDER 37 CFR §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

Respertfully submitted,

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## INFORMATION DISCLOSURE STATEMENT

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99/63580

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Atty Docket: Serial No.: Applicant: Filing Date:

Group:

62603 10/647,061 **MEARS AUGUST 22, 2003** 

U.S. PATENT DOCUMENTS								
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date	
	AA	4,485,128	11/27/84	Dalal et al.	427	85		
	AB	6,326,311	12/4/01	Ueda et al.	438	694		
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	АМ							
	AN							
	AO							
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		Document Number	Date	Country	Class	Sub Class	Translatio	
	AP	61220339A	9/30/86	Japan	H01L21	322	English Abstract	
	AQ	61145820A	7/3/86	Japan	H01L21	20	English Abstract	

EXAMINER:	DATE CONSIDERED:

WO

H01L3

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n/a

12/9/99

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.